

การตรวจสอบคุ่มรวมของประชากร

(Syntax ให้แสดงผลข้อมูลที่อยู่นอกคุ่มรวมหรือรหัส tsic ไม่ถูกต้อง)

Syntax test TSIC_L

```
*type = 01*/
```

```
/*type = 01, tsic_l = 45101 - 45405*/
```

```
IF(tsic_l >= 45101) & (tsic_l <= 45405) & /*Category 45*/
```

```
(tsic_l ~= 45101) & (tsic_l ~= 45102) & (tsic_l ~= 45103) & (tsic_l ~= 45104) &
```

```
(tsic_l ~= 45201) & (tsic_l ~= 45202) & (tsic_l ~= 45203) &
```

```
(tsic_l ~= 45301) & (tsic_l ~= 45302) & (tsic_l ~= 45303) &
```

```
(tsic_l ~= 45401) & (tsic_l ~= 45402) & (tsic_l ~= 45403) & (tsic_l ~= 45404) &
```

```
(tsic_l ~= 45405)tsic_lf=1.
```

```
EXECUTE .
```

```
/*TYPE = 02*/
```

```
/*TYPE = 02, tsic_l = 46101 - 46900*/
```

```
IF(tsic_l >= 46101) & (tsic_l <= 46900) & /*Category 46*/
```

```
(tsic_l ~= 46101) & (tsic_l ~= 46102) & (tsic_l ~= 46103) & (tsic_l ~= 46104) & (tsic_l ~= 46105) &  
(tsic_l ~= 46106) & (tsic_l ~= 46107) &
```

```
(tsic_l ~= 46108) & (tsic_l ~= 46109) & (tsic_l ~= 46201) & (tsic_l ~= 46202) & (tsic_l ~= 46203) &  
(tsic_l ~= 46204) & (tsic_l ~= 46205) &
```

```
(tsic_l ~= 46206) & (tsic_l ~= 46209) & (tsic_l ~= 46311) & (tsic_l ~= 46312) & (tsic_l ~= 46313) &  
(tsic_l ~= 46314) & (tsic_l ~= 46315) & (tsic_l ~= 46316) & (tsic_l ~= 46317) & (tsic_l ~= 46318) &
```

```
(tsic_l ~= 46319) &
```

```
(tsic_l ~= 46321) & (tsic_l ~= 46322) & (tsic_l ~= 46323) &
```

```
(tsic_l ~= 46411) & (tsic_l ~= 46412) & (tsic_l ~= 46413) & (tsic_l ~= 46414) & (tsic_l ~= 46415) &
```

```
(tsic_l ~= 46421) & (tsic_l ~= 46422) &
```

```
(tsic_l ~= 46431) & (tsic_l ~= 46432) & (tsic_l ~= 46433) & (tsic_l ~= 46434) & (tsic_l ~= 46439) &
```

```
(tsic_l ~= 46441) & (tsic_l ~= 46442) & (tsic_l ~= 46443) &
```

```
(tsic_l ~= 46491) & (tsic_l ~= 46492) & (tsic_l ~= 46493) & (tsic_l ~= 46494) & (tsic_l ~= 46495) &
```

```
(tsic_l ~= 46499) &
```

```
(tsic_l ~= 46510) &
```

```
(tsic_l ~= 46521) & (tsic_l ~= 46522) &
(tsic_l ~= 46530) &
(tsic_l ~= 46591) & (tsic_l ~= 46592) & (tsic_l ~= 46593) & (tsic_l ~= 46594) & (tsic_l ~= 46599) &
(tsic_l ~= 46611) & (tsic_l ~= 46612) & (tsic_l ~= 46613) & (tsic_l ~= 46614) &
(tsic_l ~= 46621) & (tsic_l ~= 46622) &
(tsic_l ~= 46631) & (tsic_l ~= 46632) & (tsic_l ~= 46633) & (tsic_l ~= 46634) & (tsic_l ~= 46639) &
(tsic_l ~= 46691) & (tsic_l ~= 46692) & (tsic_l ~= 46693) & (tsic_l ~= 46694) & (tsic_l ~= 46695) &
(tsic_l ~= 46699) &
(tsic_l ~= 46900)tsic_lf=1.
EXECUTE .
```

```
/*TYPE = 03*/
/*TYPE = 03, tsic_l = 47111 - 47999*/
IF(tsic_l >= 47111) & (tsic_l <= 47999) & /*Category 47*/
(tsic_l ~= 47111) & (tsic_l ~= 47112) & (tsic_l ~= 47113) & (tsic_l ~= 47114) &
(tsic_l ~= 47190) &
(tsic_l ~= 47211) & (tsic_l ~= 47212) & (tsic_l ~= 47213) & (tsic_l ~= 47214) & (tsic_l ~= 47215) &
(tsic_l ~= 47219) &
(tsic_l ~= 47221) & (tsic_l ~= 47222) &
(tsic_l ~= 47230) &
(tsic_l ~= 47300) &
(tsic_l ~= 47411) & (tsic_l ~= 47412) & (tsic_l ~= 47413) &
(tsic_l ~= 47420) &
(tsic_l ~= 47511) & (tsic_l ~= 47512) & (tsic_l ~= 47513) &
(tsic_l ~= 47521) & (tsic_l ~= 47522) & (tsic_l ~= 47523) & (tsic_l ~= 47524) & (tsic_l ~= 47525) &
(tsic_l ~= 47530) &
(tsic_l ~= 47591) & (tsic_l ~= 47592) & (tsic_l ~= 47593) & (tsic_l ~= 47594) & (tsic_l ~= 47595) &
(tsic_l ~= 47599) &
(tsic_l ~= 47611) & (tsic_l ~= 47612) &
(tsic_l ~= 47620) &
(tsic_l ~= 47630) &
(tsic_l ~= 47640) &
(tsic_l ~= 47691) & (tsic_l ~= 47699) &
(tsic_l ~= 47711) & (tsic_l ~= 47712) & (tsic_l ~= 47713) &
(tsic_l ~= 47721) & (tsic_l ~= 47722) & (tsic_l ~= 47723) &
```

```
(tsic_l ~= 47731) & (tsic_l ~= 47732) & (tsic_l ~= 47733) & (tsic_l ~= 47734) & (tsic_l ~= 47735) &
(tsic_l ~= 47739) &
(tsic_l ~= 47741) & (tsic_l ~= 47742) & (tsic_l ~= 47743) & (tsic_l ~= 47744) & (tsic_l ~= 47745) &
(tsic_l ~= 47749) &
(tsic_l ~= 47911) & (tsic_l ~= 47912) &
(tsic_l ~= 47991) tsic_lf=1.
EXECUTE .
```

```
/*TYPE = 04*/
/*TYPE = 04, tsic_l = 55101 - 56302*/
IF(tsic_l >= 55101) & (tsic_l <= 56304) & /*Category 55*/
(tsic_l ~= 55101) & (tsic_l ~= 55102) & (tsic_l ~= 55103) & (tsic_l ~= 55109) &
(tsic_l ~= 55200) & (tsic_l ~= 55901) & (tsic_l ~= 55909) & /*Category 56*/
(tsic_l ~= 56101) &
(tsic_l ~= 56210) &
(tsic_l ~= 56291) & (tsic_l ~= 56292) & (tsic_l ~= 56299) &
(tsic_l ~= 56301) & (tsic_l ~= 56302) tsic_lf=1.
EXECUTE .
```

```
/*TYPE = 05*/
/*TYPE = 05, tsic_l = 58111 - 63990*/
IF(tsic_l >= 58111) & (tsic_l <= 63990) & /*Category 58*/
(tsic_l ~= 58111) & (tsic_l ~= 58112) & (tsic_l ~= 58113) & (tsic_l ~= 58114)&
(tsic_l ~= 58121) & (tsic_l ~= 58122) &
(tsic_l ~= 58131) & (tsic_l ~= 58132) & (tsic_l ~= 58133) & (tsic_l ~= 58134)&
(tsic_l ~= 58191) & (tsic_l ~= 58192) & (tsic_l ~= 58193) &
(tsic_l ~= 58201) & (tsic_l ~= 58202) & (tsic_l ~= 58203) & /*Category 59*/
(tsic_l ~= 59111) & (tsic_l ~= 59112) &
(tsic_l ~= 59121) & (tsic_l ~= 59122) & (tsic_l ~= 59129) &
(tsic_l ~= 59131) & (tsic_l ~= 59132) &
(tsic_l ~= 59140) &
(tsic_l ~= 59201) & (tsic_l ~= 59202) & (tsic_l ~= 59203) & /*Category 60*/
(tsic_l ~= 60101) & (tsic_l ~= 60102) &
(tsic_l ~= 60201) & (tsic_l ~= 60202) & (tsic_l ~= 60203) & /*Category 61*/
```

```
(tsic_l ~= 61101) & (tsic_l ~= 61102) & (tsic_l ~= 61109) &
(tsic_l ~= 61201) & (tsic_l ~= 61202) & (tsic_l ~= 61209) &
(tsic_l ~= 61301) & (tsic_l ~= 61302) &
(tsic_l ~= 61900) & /*Category 62*/
(tsic_l ~= 62011) & (tsic_l ~= 62012) &
(tsic_l ~= 62021) & (tsic_l ~= 62022) & (tsic_l ~= 62023) &
(tsic_l ~= 62090) & /*Category 63*/
(tsic_l ~= 63111) & (tsic_l ~= 63112) &
(tsic_l ~= 63120) &
(tsic_l ~= 63911) & (tsic_l ~= 63912) &
(tsic_l ~= 63990)tsic_lf=1.
```

EXECUTE .

```
/*TYPE = 06*/
```

```
/*TYPE = 06, tsic_l = 68101 - 68202*/
```

```
IF(tsic_l >= 68101) & (tsic_l <= 68202) & /*Category 68*/
```

```
(tsic_l ~= 68101) & (tsic_l ~= 68102) & (tsic_l ~= 68103) & (tsic_l ~= 68104) &
(tsic_l ~= 68201) & (tsic_l ~= 68202)tsic_lf=1.
```

EXECUTE .

```
/*TYPE = 07*/
```

```
/*TYPE = 07, tsic_l = 69100 - 74909*/
```

```
IF(tsic_l >= 69100) & (tsic_l <= 74909) & /*Category 69*/
```

```
(tsic_l ~= 69100) & (tsic_l ~= 69200) & /*Category 70*/
(tsic_l ~= 70100) &
(tsic_l ~= 70201) & (tsic_l ~= 70202) & (tsic_l ~= 70209) & /*Category 71*/
(tsic_l ~= 71101) & (tsic_l ~= 71102) & (tsic_l ~= 71103) &
(tsic_l ~= 71201) & (tsic_l ~= 71202) & (tsic_l ~= 71209) & /*Category 72*/
(tsic_l ~= 72101) & (tsic_l ~= 72102) & (tsic_l ~= 72109) &
(tsic_l ~= 72200) & /*Category 73*/
(tsic_l ~= 73101) & (tsic_l ~= 73102) &
(tsic_l ~= 73200) & /*Category 74*/
(tsic_l ~= 74101) & (tsic_l ~= 74109) &
(tsic_l ~= 74200) &
```

```
(tsic_l ~= 74901) & (tsic_l ~= 74902) & (tsic_l ~= 74909)tsic_lf=1.  
EXECUTE .
```

```
/*TYPE = 08*/
```

```
/*TYPE = 08, tsic_l = 77101 - 82990*/
```

```
IF(tsic_l >= 77101) & (tsic_l <= 82990) & /*Category 77*/
```

```
(tsic_l ~= 77101) & (tsic_l ~= 77109) &
```

```
(tsic_l ~= 77210) &
```

```
(tsic_l ~= 77220) &
```

```
(tsic_l ~= 77291) & (tsic_l ~= 77299) &
```

```
(tsic_l ~= 77301) & (tsic_l ~= 77302) & (tsic_l ~= 77303) & (tsic_l ~= 77304) & (tsic_l ~= 77305) &
```

```
(tsic_l ~= 77306) & (tsic_l ~= 77309) &
```

```
(tsic_l ~= 77400) & /*Category 78*/
```

```
(tsic_l ~= 78101) & (tsic_l ~= 78109) &
```

```
(tsic_l ~= 78200) &
```

```
(tsic_l ~= 78300) & /*Category 79*/
```

```
(tsic_l ~= 79110) &
```

```
(tsic_l ~= 79120) &
```

```
(tsic_l ~= 79901) &
```

```
(tsic_l ~= 79909) & /*Category 80*/
```

```
(tsic_l ~= 80100) &
```

```
(tsic_l ~= 80200) &
```

```
(tsic_l ~= 80300) & /*Category 81*/
```

```
(tsic_l ~= 81100) &
```

```
(tsic_l ~= 81210) &
```

```
(tsic_l ~= 81291) & (tsic_l ~= 81292) & (tsic_l ~= 81293) & (tsic_l ~= 81299) &
```

```
(tsic_l ~= 81300) & /*Category 82*/
```

```
(tsic_l ~= 82110) &
```

```
(tsic_l ~= 82191) & (tsic_l ~= 82199) &
```

```
(tsic_l ~= 82200) &
```

```
(tsic_l ~= 82301) & (tsic_l ~= 82302) &
```

```
(tsic_l ~= 82911) & (tsic_l ~= 82912) &
```

```
(tsic_l ~= 82920) &
```

```
(tsic_l ~= 82990)tsic_lf=1.
```

```
EXECUTE .
```

```
/*TYPE = 09*/
/*TYPE = 09, tsic_l = 90001 - 93299*/
IF(tsic_l >= 90001) & (tsic_l <= 93299) & /*Category 90*/
  (tsic_l ~= 90001) & (tsic_l ~= 90002) & /*Category 91*/
  (tsic_l ~= 91011) & (tsic_l ~= 91012) &
  (tsic_l ~= 91021) & (tsic_l ~= 91022) &
  (tsic_l ~= 91031) & (tsic_l ~= 91032) &
  (tsic_l ~= 92001) & (tsic_l ~= 92009) & /*Category 93*/
  (tsic_l ~= 93111) & (tsic_l ~= 93112) &
  (tsic_l ~= 93120) &
  (tsic_l ~= 93191) & (tsic_l ~= 93192) & (tsic_l ~= 93199) &
  (tsic_l ~= 93210) &
  (tsic_l ~= 93291) & (tsic_l ~= 93292) & (tsic_l ~= 93293) & (tsic_l ~= 93299)tsic_lf=1.
EXECUTE .
```

```
/*TYPE = 10*/
/*TYPE = 10, tsic_l = 95110 - 96309*/
IF(tsic_l >= 95110) & (tsic_l <= 96309) & /*Category 95*/
  (tsic_l ~= 95110) &
  (tsic_l ~= 95120) &
  (tsic_l ~= 95210) &
  (tsic_l ~= 95220) &
  (tsic_l ~= 95230) &
  (tsic_l ~= 95240) &
  (tsic_l ~= 95291) & (tsic_l ~= 95292) & (tsic_l ~= 95293) & (tsic_l ~= 95294) & (tsic_l ~= 95295) &
  (tsic_l ~= 95299) & /*Category 96*/
  (tsic_l ~= 96101) & (tsic_l ~= 96102) & (tsic_l ~= 96103) & (tsic_l ~= 96104) & (tsic_l ~= 96109) &
  (tsic_l ~= 96201) & (tsic_l ~= 96202) & (tsic_l ~= 96203) &
  (tsic_l ~= 96301) & (tsic_l ~= 96302) & (tsic_l ~= 96303) & (tsic_l ~= 96304) & (tsic_l ~= 96305) &
  (tsic_l ~= 96309)tsic_lf=1.
EXECUTE .
```

```
/*TYPE = 11*/
/*TYPE = 11, tsic_l = 10111 - 33200*/
IF(tsic_l >= 10111) & (tsic_l <= 33200) & /*Category 10*/
  (tsic_l ~= 10111) & (tsic_l ~= 10112) &
  (tsic_l ~= 10120) &
  (tsic_l ~= 10131) & (tsic_l ~= 10132) & (tsic_l ~= 10133) & (tsic_l ~= 10134) & (tsic_l ~= 10139) &
  (tsic_l ~= 10211) & (tsic_l ~= 10212) &
  (tsic_l ~= 10221) & (tsic_l ~= 10222) &
  (tsic_l ~= 10291) & (tsic_l ~= 10292) & (tsic_l ~= 10293) & (tsic_l ~= 10294) & (tsic_l ~= 10295) &
  (tsic_l ~= 10299) &
  (tsic_l ~= 10301) & (tsic_l ~= 10302) & (tsic_l ~= 10303) & (tsic_l ~= 10304) & (tsic_l ~= 10305) &
  (tsic_l ~= 10306) & (tsic_l ~= 10307) & (tsic_l ~= 10309) &
  (tsic_l ~= 10411) & (tsic_l ~= 10412) & (tsic_l ~= 10413) & (tsic_l ~= 10414) & (tsic_l ~= 10419) &
  (tsic_l ~= 10420) &
  (tsic_l ~= 10491) & (tsic_l ~= 10499) &
  (tsic_l ~= 10501) & (tsic_l ~= 10502) & (tsic_l ~= 10503) & (tsic_l ~= 10504) & (tsic_l ~= 10505) &
  (tsic_l ~= 10509) &
  (tsic_l ~= 10611) & (tsic_l ~= 10612) & (tsic_l ~= 10613) & (tsic_l ~= 10614) & (tsic_l ~= 10615) &
  (tsic_l ~= 10616) & (tsic_l ~= 10617) & (tsic_l ~= 10619) &
  (tsic_l ~= 10621) & (tsic_l ~= 10622) & (tsic_l ~= 10623) & (tsic_l ~= 10629) &
  (tsic_l ~= 10711) & (tsic_l ~= 10712) & (tsic_l ~= 10713) &
  (tsic_l ~= 10721) & (tsic_l ~= 10722) & (tsic_l ~= 10723) &
  (tsic_l ~= 10731) & (tsic_l ~= 10732) & (tsic_l ~= 10733) & (tsic_l ~= 10734) & (tsic_l ~= 10739) &
  (tsic_l ~= 10741) & (tsic_l ~= 10742) & (tsic_l ~= 10743) & (tsic_l ~= 10749) &
  (tsic_l ~= 10751) & (tsic_l ~= 10752) &
  (tsic_l ~= 10761) & (tsic_l ~= 10762) & (tsic_l ~= 10769) &
  (tsic_l ~= 10771) & (tsic_l ~= 10772) & (tsic_l ~= 10773) & (tsic_l ~= 10774) & (tsic_l ~= 10775) &
  (tsic_l ~= 10779) &
  (tsic_l ~= 10791) & (tsic_l ~= 10792) & (tsic_l ~= 10793) & (tsic_l ~= 10794) & (tsic_l ~= 10795) &
  (tsic_l ~= 10796) & (tsic_l ~= 10799) &
  (tsic_l ~= 10801) & (tsic_l ~= 10802) & /*Category 11*/
  (tsic_l ~= 11011) & (tsic_l ~= 11012) &
  (tsic_l ~= 11021) & (tsic_l ~= 11022) & (tsic_l ~= 11029) &
  (tsic_l ~= 11030) &
  (tsic_l ~= 11041) & (tsic_l ~= 11042) & (tsic_l ~= 11043) & (tsic_l ~= 11044) & (tsic_l ~= 11049) &
/*Category 12*/
```

```
(tsic_l ~= 12001) & (tsic_l ~= 12002) & (tsic_l ~= 12009) & /*Category 13*/
(tsic_l ~= 13111) & (tsic_l ~= 13112) & (tsic_l ~= 13113) &
(tsic_l ~= 13121) & (tsic_l ~= 13122) & (tsic_l ~= 13129) &
(tsic_l ~= 13131) & (tsic_l ~= 13132) & (tsic_l ~= 13139) &
(tsic_l ~= 13910) &
(tsic_l ~= 13921) & (tsic_l ~= 13922) & (tsic_l ~= 13929) &
(tsic_l ~= 13930) &
(tsic_l ~= 13940) &
(tsic_l ~= 13991) & (tsic_l ~= 13992) & (tsic_l ~= 13999) & /*Category 14*/
(tsic_l ~= 14111) & (tsic_l ~= 14112) & (tsic_l ~= 14113) & (tsic_l ~= 14114) & (tsic_l ~= 14115) &
(tsic_l ~= 14116) &
(tsic_l ~= 14120) &
(tsic_l ~= 14131) & (tsic_l ~= 14139) &
(tsic_l ~= 14200) &
(tsic_l ~= 14301) & (tsic_l ~= 14302) & /*Category 15*/
(tsic_l ~= 15110) &
(tsic_l ~= 15121) & (tsic_l ~= 15122) & (tsic_l ~= 15129) &
(tsic_l ~= 15201) & (tsic_l ~= 15202) & (tsic_l ~= 15203) & (tsic_l ~= 15204) & (tsic_l ~= 15209) &
/*Category 16*/
(tsic_l ~= 16101) & (tsic_l ~= 16102) &
(tsic_l ~= 16210) &
(tsic_l ~= 16220) &
(tsic_l ~= 16230) &
(tsic_l ~= 16291) & (tsic_l ~= 16292) & (tsic_l ~= 16299) & /*Category 17*/
(tsic_l ~= 17011) & (tsic_l ~= 17012) &
(tsic_l ~= 17020) &
(tsic_l ~= 17091) & (tsic_l ~= 17092) & (tsic_l ~= 17099) & /*Category 18*/
(tsic_l ~= 18111) & (tsic_l ~= 18112) & (tsic_l ~= 18119) &
(tsic_l ~= 18121) & (tsic_l ~= 18122) &
(tsic_l ~= 18200) & /*Category 19*/
(tsic_l ~= 19100) &
(tsic_l ~= 19201) & (tsic_l ~= 19202) & (tsic_l ~= 19209) & /*Category 20*/
(tsic_l ~= 20111) & (tsic_l ~= 20112) & (tsic_l ~= 20113) & (tsic_l ~= 20114) & (tsic_l ~= 20115) &
(tsic_l ~= 20121) & (tsic_l ~= 20122) &
(tsic_l ~= 20131) & (tsic_l ~= 20132) &
(tsic_l ~= 20210) &
```


(tsic_l ~= 20221) & (tsic_l ~= 20222) &
(tsic_l ~= 20231) & (tsic_l ~= 20232) &
(tsic_l ~= 20291) & (tsic_l ~= 20292) & (tsic_l ~= 20293) & (tsic_l ~= 20294) & (tsic_l ~= 20299) &
(tsic_l ~= 20300) & /*Category 21*/
(tsic_l ~= 21001) & (tsic_l ~= 21002) & /*Category 22*/
(tsic_l ~= 22111) & (tsic_l ~= 22112) &
(tsic_l ~= 22191) & (tsic_l ~= 22192) & (tsic_l ~= 22193) & (tsic_l ~= 22199) &
(tsic_l ~= 22210) & (tsic_l ~= 22220) & (tsic_l ~= 22230) &
(tsic_l ~= 22291) & (tsic_l ~= 22292) & (tsic_l ~= 22299) & /*Category 23*/
(tsic_l ~= 23101) & (tsic_l ~= 23102) & (tsic_l ~= 23103) & (tsic_l ~= 23109) &
(tsic_l ~= 23910) &
(tsic_l ~= 23921) & (tsic_l ~= 23922) & (tsic_l ~= 23923) & (tsic_l ~= 23929) &
(tsic_l ~= 23931) & (tsic_l ~= 23932) & (tsic_l ~= 23933) & (tsic_l ~= 23939) &
(tsic_l ~= 23941) & (tsic_l ~= 23942) &
(tsic_l ~= 23951) & (tsic_l ~= 23952) & (tsic_l ~= 23953) & (tsic_l ~= 23954) & (tsic_l ~= 23959) &
(tsic_l ~= 23961) & (tsic_l ~= 23969) &
(tsic_l ~= 23991) & (tsic_l ~= 23992) & (tsic_l ~= 23999) & /*Category 24*/
(tsic_l ~= 24101) & (tsic_l ~= 24102) & (tsic_l ~= 24103) & (tsic_l ~= 24109) &
(tsic_l ~= 24201) & (tsic_l ~= 24202) & (tsic_l ~= 24203) & (tsic_l ~= 24204) & (tsic_l ~= 24209) &
(tsic_l ~= 24311) & (tsic_l ~= 24312) &
(tsic_l ~= 24320) & /*Category 25*/
(tsic_l ~= 25111) & (tsic_l ~= 25112) & (tsic_l ~= 25113) & (tsic_l ~= 25119) &
(tsic_l ~= 25121) & (tsic_l ~= 25122) & (tsic_l ~= 25129) &
(tsic_l ~= 25130) &
(tsic_l ~= 25201) & (tsic_l ~= 25209) &
(tsic_l ~= 25910) &
(tsic_l ~= 25921) & (tsic_l ~= 25922) &
(tsic_l ~= 25931) & (tsic_l ~= 25932) & (tsic_l ~= 25939) &
(tsic_l ~= 25941) & (tsic_l ~= 25949) &
(tsic_l ~= 25951) & (tsic_l ~= 25952) &
(tsic_l ~= 25991) & (tsic_l ~= 25992) & (tsic_l ~= 25993) & (tsic_l ~= 25999) & /*Category 26*/
(tsic_l ~= 26101) & (tsic_l ~= 26102) & (tsic_l ~= 26103) & (tsic_l ~= 26104) & (tsic_l ~= 26109) &
(tsic_l ~= 26201) & (tsic_l ~= 26202) & (tsic_l ~= 26203) & (tsic_l ~= 26209) &
(tsic_l ~= 26301) & (tsic_l ~= 26302) & (tsic_l ~= 26303) & (tsic_l ~= 26309) &
(tsic_l ~= 26401) & (tsic_l ~= 26402) & (tsic_l ~= 26403) & (tsic_l ~= 26409) &
(tsic_l ~= 26511) & (tsic_l ~= 26512) &

(tsic_l ~= 26521) & (tsic_l ~= 26529) &
(tsic_l ~= 26600) &
(tsic_l ~= 26701) & (tsic_l ~= 26702) & (tsic_l ~= 26703) &
(tsic_l ~= 26800) & /*Category 27*/
(tsic_l ~= 27101) & (tsic_l ~= 27102) & (tsic_l ~= 27103) &
(tsic_l ~= 27200) &
(tsic_l ~= 27310) & (tsic_l ~= 27320) & (tsic_l ~= 27330) &
(tsic_l ~= 27401) & (tsic_l ~= 27409) &
(tsic_l ~= 27501) & (tsic_l ~= 27502) & (tsic_l ~= 27503) &
(tsic_l ~= 27901) & (tsic_l ~= 27902) & (tsic_l ~= 27909) & /*Category 28*/
(tsic_l ~= 28110) &
(tsic_l ~= 28120) &
(tsic_l ~= 28131) & (tsic_l ~= 28132) &
(tsic_l ~= 28140) &
(tsic_l ~= 28150) &
(tsic_l ~= 28160) &
(tsic_l ~= 28170) &
(tsic_l ~= 28180) &
(tsic_l ~= 28191) & (tsic_l ~= 28199) &
(tsic_l ~= 28211) & (tsic_l ~= 28219) &
(tsic_l ~= 28221) & (tsic_l ~= 28229) &
(tsic_l ~= 28230) &
(tsic_l ~= 28240) &
(tsic_l ~= 28250) &
(tsic_l ~= 28261) & (tsic_l ~= 28269) &
(tsic_l ~= 28291) &
(tsic_l ~= 28292) & (tsic_l ~= 28299) & /*Category 29*/
(tsic_l ~= 29101) & (tsic_l ~= 29102) & (tsic_l ~= 29103) & (tsic_l ~= 29104) & (tsic_l ~= 29109) &
(tsic_l ~= 29201) & (tsic_l ~= 29202) & (tsic_l ~= 29203) &
(tsic_l ~= 29301) & (tsic_l ~= 29302) & (tsic_l ~= 29309) & /*Category 30*/
(tsic_l ~= 30110) &
(tsic_l ~= 30120) &
(tsic_l ~= 30200) &
(tsic_l ~= 30300) &
(tsic_l ~= 30400) &
(tsic_l ~= 30911) & (tsic_l ~= 30912) &

```
(tsic_l ~= 30921) & (tsic_l ~= 30922) &
(tsic_l ~= 30990) & /*Category 31*/
(tsic_l ~= 31001) & (tsic_l ~= 31002) & (tsic_l ~= 31003) & (tsic_l ~= 31009) & /*Category 32*/
(tsic_l ~= 32111) & (tsic_l ~= 32112) & (tsic_l ~= 32113) &
(tsic_l ~= 32120) &
(tsic_l ~= 32201) & (tsic_l ~= 32209) &
(tsic_l ~= 32301) & (tsic_l ~= 32302) & (tsic_l ~= 32303) & (tsic_l ~= 32309) &
(tsic_l ~= 32401) & (tsic_l ~= 32402) & (tsic_l ~= 32409) &
(tsic_l ~= 32501) & (tsic_l ~= 32502) &
(tsic_l ~= 32901) & (tsic_l ~= 32902) & (tsic_l ~= 32903) & (tsic_l ~= 32904) & (tsic_l ~= 32909) &
/*Category 33*/
(tsic_l ~= 33110) &
(tsic_l ~= 33121) & (tsic_l ~= 33122) &
(tsic_l ~= 33131) & (tsic_l ~= 33132) & (tsic_l ~= 33133) &
(tsic_l ~= 33141) & (tsic_l ~= 33142) & (tsic_l ~= 33149) &
(tsic_l ~= 33151) & (tsic_l ~= 33152) & (tsic_l ~= 33159) &
(tsic_l ~= 33190) &
(tsic_l ~= 33200)tsic_lf=1.
EXECUTE .
```

```
/*TYPE = 12*/
/*TYPE = 12, tsic_l = 37000 - 39000*/
IF(tsic_l >= 37000) & (tsic_l <= 39000) & /*Category 37*/
(tsic_l ~= 37000) & /*Category 38*/
(tsic_l ~= 38110) & (tsic_l ~= 38120) &
(tsic_l ~= 38211) & (tsic_l ~= 38212) & (tsic_l ~= 38213) & (tsic_l ~= 38219) &
(tsic_l ~= 38221) & (tsic_l ~= 38222) & (tsic_l ~= 38300) &
(tsic_l ~= 39000)tsic_lf=1.
EXECUTE .
```

```
/*TYPE = 13*/
/*TYPE = 13, tsic_l = 41001 - 43909*/
IF(tsic_l >= 41001) & (tsic_l <= 43909) & /*Category 41*/
(tsic_l ~= 41001) & (tsic_l ~= 41002) & /*Category 42*/
```

```
(tsic_l ~= 42101) & (tsic_l ~= 42102) &
(tsic_l ~= 42201) & (tsic_l ~= 42202) &
(tsic_l ~= 42901) & (tsic_l ~= 42909) & /*Category 43*/
(tsic_l ~= 43110) &
(tsic_l ~= 43120) &
(tsic_l ~= 43210) &
(tsic_l ~= 43221) & (tsic_l ~= 43222) & (tsic_l ~= 43223) &
(tsic_l ~= 43291) & (tsic_l ~= 43299) &
(tsic_l ~= 43301) & (tsic_l ~= 43302) & (tsic_l ~= 43303) & (tsic_l ~= 43309) &
(tsic_l ~= 43901) & (tsic_l ~= 43909)tsic_lf=1.
EXECUTE .
```

```
/*TYPE = 14*/
/*TYPE = 14, tsic_l = 49110 - 52299*/
IF(tsic_l >= 49110) & (tsic_l <= 52299) & /*Category 49*/
(tsic_l ~= 49201) & (tsic_l ~= 49202) & (tsic_l ~= 49203) & (tsic_l ~= 49204) & (tsic_l ~= 49209) &
(tsic_l ~= 49310) &
(tsic_l ~= 49329) &
(tsic_l ~= 49331) & (tsic_l ~= 49332) & (tsic_l ~= 49333) & (tsic_l ~= 49334) & (tsic_l ~= 49339) &
/*Category 52*/
(tsic_l ~= 52101) & (tsic_l ~= 52102) & (tsic_l ~= 52109) &
(tsic_l ~= 52291) tsic_lf=1.
EXECUTE .
```

```
/*TYPE = 15*/
/*TYPE = 15, tsic_l = 86101 - 86102*/
IF(tsic_l >= 86101) & (tsic_l <= 86102) & /*Category 86*/
(tsic_l ~= 86101) & (tsic_l ~= 86102)tsic_lf=1.
EXECUTE .
```

```
IF((tsic_l >= 10803) & (tsic_l <= 11010))|((tsic_l >= 11050) & (tsic_l <= 12000))|((tsic_l >= 12010) &
(tsic_l <= 13110))|
```

((tsic_l >= 14000) & (tsic_l <= 14101))|((tsic_l >= 14303) & (tsic_l <= 15109))|((tsic_l >= 15210) & (tsic_l <= 16100))|
((tsic_l >= 16300) & (tsic_l <= 17010))|((tsic_l >= 17100) & (tsic_l <= 18110))|((tsic_l >= 18201) & (tsic_l <= 19099))|
((tsic_l >= 19210) & (tsic_l <= 20110))|((tsic_l >= 20301) & (tsic_l <= 21000))|((tsic_l >= 21003) & (tsic_l <= 22110))|
((tsic_l >= 22300) & (tsic_l <= 23100))|((tsic_l >= 24000) & (tsic_l <= 24100))|((tsic_l >= 24321) & (tsic_l <= 25110))|
((tsic_l >= 26000) & (tsic_l <= 26100))|((tsic_l >= 26801) & (tsic_l <= 27100))|((tsic_l >= 27910) & (tsic_l <= 28109))|
((tsic_l >= 28300) & (tsic_l <= 29100))|((tsic_l >= 29310) & (tsic_l <= 30109))|((tsic_l >= 30991) & (tsic_l <= 31000))|
((tsic_l >= 31010) & (tsic_l <= 32110))|((tsic_l >= 32910) & (tsic_l <= 33109))|((tsic_l >= 33201) & (tsic_l <= 36999))|
((tsic_l >= 37001) & (tsic_l <= 38109))|((tsic_l >= 38301) & (tsic_l <= 38999))|((tsic_l >= 39001) & (tsic_l <= 41000))|
((tsic_l >= 41003) & (tsic_l <= 42100))|((tsic_l >= 42910) & (tsic_l <= 43109))|((tsic_l >= 43910) & (tsic_l <= 45100))|
((tsic_l >= 45406) & (tsic_l <= 46100))|((tsic_l >= 46901) & (tsic_l <= 47110))|((tsic_l >= 48000) & (tsic_l <= 49109))|
((tsic_l >= 49399) & (tsic_l <= 52100))|((tsic_l >= 52300) & (tsic_l <= 55100))|((tsic_l >= 55910) & (tsic_l <= 56100))|
((tsic_l >= 56305) & (tsic_l <= 58110))|((tsic_l >= 58204) & (tsic_l <= 59110))|((tsic_l >= 59204) & (tsic_l <= 60100))|
((tsic_l >= 60204) & (tsic_l <= 61100))|((tsic_l >= 61901) & (tsic_l <= 62010))|((tsic_l >= 62091) & (tsic_l <= 63110))|
((tsic_l >= 63991) & (tsic_l <= 68100))|((tsic_l >= 68203) & (tsic_l <= 69099))|((tsic_l >= 69201) & (tsic_l <= 70099))|
((tsic_l >= 70210) & (tsic_l <= 71100))|((tsic_l >= 68203) & (tsic_l <= 69099))|((tsic_l >= 69201) & (tsic_l <= 70099))|
((tsic_l >= 71210) & (tsic_l <= 72100))|((tsic_l >= 72201) & (tsic_l <= 73100))|((tsic_l >= 73201) & (tsic_l <= 74100))|
((tsic_l >= 74910) & (tsic_l <= 77100))|((tsic_l >= 77401) & (tsic_l <= 78100))|((tsic_l >= 78301) & (tsic_l <= 79109))|
((tsic_l >= 79910) & (tsic_l <= 80099))|((tsic_l >= 80301) & (tsic_l <= 81099))|((tsic_l >= 81301) & (tsic_l <= 82109))|

```
((tsic_l >= 82991) & (tsic_l <= 86100))|((tsic_l >= 86103) & (tsic_l <= 90000))|((tsic_l >= 90003) &
(tsic_l <= 91010))|
((tsic_l >= 91033) & (tsic_l <= 92000))|((tsic_l >= 92010) & (tsic_l <= 93110))|((tsic_l >= 93300) &
(tsic_l <= 95109))|
((tsic_l >= 95300) & (tsic_l <= 96100))|((tsic_l >= 96310) & (tsic_l <= 99999))|((tsic_l >= 00000) &
(tsic_l <= 10110))tsic_lf=1.
EXECUTE .
```

```
SORT CASES BY
  tsic_lf (D) .
```

Syntax test TSIC_R

```
/*type = 01*/  
/*type = 01, tsic_r = 45101 - 45405*/  
IF(tsic_r >= 45101) & (tsic_r <= 45405) & /*Category 45*/  
  (tsic_r ~= 45101) & (tsic_r ~= 45102) & (tsic_r ~= 45103) & (tsic_r ~= 45104) &  
  (tsic_r ~= 45201) & (tsic_r ~= 45202) & (tsic_r ~= 45203) &  
  (tsic_r ~= 45301) & (tsic_r ~= 45302) & (tsic_r ~= 45303) &  
  (tsic_r ~= 45401) & (tsic_r ~= 45402) & (tsic_r ~= 45403) & (tsic_r ~= 45404) & (tsic_r ~= 45405)tsic_rf=1.  
EXECUTE .
```

```
/*TYPE = 02*/  
/*TYPE = 02, tsic_r = 46101 - 46900*/  
IF(tsic_r >= 46101) & (tsic_r <= 46900) & /*Category 46*/  
  (tsic_r ~= 46101) & (tsic_r ~= 46102) & (tsic_r ~= 46103) & (tsic_r ~= 46104) & (tsic_r ~= 46105) &  
(tsic_r ~= 46106) & (tsic_r ~= 46107) &  
  (tsic_r ~= 46108) & (tsic_r ~= 46109) & (tsic_r ~= 46201) & (tsic_r ~= 46202) & (tsic_r ~= 46203) &  
(tsic_r ~= 46204) & (tsic_r ~= 46205) &  
  (tsic_r ~= 46206) & (tsic_r ~= 46209) & (tsic_r ~= 46311) & (tsic_r ~= 46312) & (tsic_r ~= 46313) &  
  (tsic_r ~= 46314) & (tsic_r ~= 46315) & (tsic_r ~= 46316) & (tsic_r ~= 46317) & (tsic_r ~= 46318) &  
(tsic_r ~= 46319) &  
  (tsic_r ~= 46321) & (tsic_r ~= 46322) & (tsic_r ~= 46323) &  
  (tsic_r ~= 46411) & (tsic_r ~= 46412) & (tsic_r ~= 46413) & (tsic_r ~= 46414) & (tsic_r ~= 46415) &  
  (tsic_r ~= 46421) & (tsic_r ~= 46422) &  
  (tsic_r ~= 46431) & (tsic_r ~= 46432) & (tsic_r ~= 46433) & (tsic_r ~= 46434) & (tsic_r ~= 46439) &  
  (tsic_r ~= 46441) & (tsic_r ~= 46442) & (tsic_r ~= 46443) &  
  (tsic_r ~= 46491) & (tsic_r ~= 46492) & (tsic_r ~= 46493) & (tsic_r ~= 46494) & (tsic_r ~= 46495) &  
(tsic_r ~= 46499) &  
  (tsic_r ~= 46510) &  
  (tsic_r ~= 46521) & (tsic_r ~= 46522) &  
  (tsic_r ~= 46530) &  
  (tsic_r ~= 46591) & (tsic_r ~= 46592) & (tsic_r ~= 46593) & (tsic_r ~= 46594) & (tsic_r ~= 46599) &  
  (tsic_r ~= 46611) & (tsic_r ~= 46612) & (tsic_r ~= 46613) & (tsic_r ~= 46614) &  
  (tsic_r ~= 46621) & (tsic_r ~= 46622) &
```

```
(tsic_r ~= 46631) & (tsic_r ~= 46632) & (tsic_r ~= 46633) & (tsic_r ~= 46634) & (tsic_r ~= 46639) &
(tsic_r ~= 46691) & (tsic_r ~= 46692) & (tsic_r ~= 46693) & (tsic_r ~= 46694) & (tsic_r ~= 46695) &
(tsic_r ~= 46699) &
(tsic_r ~= 46900)tsic_rf=1.
EXECUTE .
```

```
/*TYPE = 03*/
/*TYPE = 03, tsic_r = 47111 - 47999*/
IF(tsic_r >= 47111) & (tsic_r <= 47999) & /*Category 47*/
(tsic_r ~= 47111) & (tsic_r ~= 47112) & (tsic_r ~= 47113) & (tsic_r ~= 47114) &
(tsic_r ~= 47190) &
(tsic_r ~= 47211) & (tsic_r ~= 47212) & (tsic_r ~= 47213) & (tsic_r ~= 47214) & (tsic_r ~= 47215) &
(tsic_r ~= 47219) &
(tsic_r ~= 47221) & (tsic_r ~= 47222) &
(tsic_r ~= 47230) &
(tsic_r ~= 47300) &
(tsic_r ~= 47411) & (tsic_r ~= 47412) & (tsic_r ~= 47413) &
(tsic_r ~= 47420) &
(tsic_r ~= 47511) & (tsic_r ~= 47512) & (tsic_r ~= 47513) &
(tsic_r ~= 47521) & (tsic_r ~= 47522) & (tsic_r ~= 47523) & (tsic_r ~= 47524) & (tsic_r ~= 47525) &
(tsic_r ~= 47530) &
(tsic_r ~= 47591) & (tsic_r ~= 47592) & (tsic_r ~= 47593) & (tsic_r ~= 47594) & (tsic_r ~= 47595) &
(tsic_r ~= 47599) &
(tsic_r ~= 47611) & (tsic_r ~= 47612) &
(tsic_r ~= 47620) &
(tsic_r ~= 47630) &
(tsic_r ~= 47640) &
(tsic_r ~= 47691) & (tsic_r ~= 47699) &
(tsic_r ~= 47711) & (tsic_r ~= 47712) & (tsic_r ~= 47713) &
(tsic_r ~= 47721) & (tsic_r ~= 47722) & (tsic_r ~= 47723) &
(tsic_r ~= 47731) & (tsic_r ~= 47732) & (tsic_r ~= 47733) & (tsic_r ~= 47734) & (tsic_r ~= 47735) &
(tsic_r ~= 47739) &
(tsic_r ~= 47741) & (tsic_r ~= 47742) & (tsic_r ~= 47743) & (tsic_r ~= 47744) & (tsic_r ~= 47745) &
(tsic_r ~= 47749) &
(tsic_r ~= 47911) & (tsic_r ~= 47912) &
```



```
(tsic_r ~= 47991) tsic_rf=1.  
EXECUTE .
```

```
/*TYPE = 04*/  
/*TYPE = 04, tsic_r = 55101 - 56302*/  
IF(tsic_r >= 55101) & (tsic_r <= 56304) & /*Category 55*/  
  (tsic_r ~= 55101) & (tsic_r ~= 55102) & (tsic_r ~= 55103) & (tsic_r ~= 55109) &  
  (tsic_r ~= 55200) & (tsic_r ~= 55901) & (tsic_r ~= 55909) & /*Category 56*/  
  (tsic_r ~= 56101) &  
  (tsic_r ~= 56210) &  
  (tsic_r ~= 56291) & (tsic_r ~= 56292) & (tsic_r ~= 56299) &  
  (tsic_r ~= 56301) & (tsic_r ~= 56302) tsic_rf=1.  
EXECUTE .
```

```
/*TYPE = 05*/  
/*TYPE = 05, tsic_r = 58111 - 63990*/  
IF(tsic_r >= 58111) & (tsic_r <= 63990) & /*Category 58*/  
  (tsic_r ~= 58111) & (tsic_r ~= 58112) & (tsic_r ~= 58113) & (tsic_r ~= 58114)&  
  (tsic_r ~= 58121) & (tsic_r ~= 58122) &  
  (tsic_r ~= 58131) & (tsic_r ~= 58132) & (tsic_r ~= 58133) & (tsic_r ~= 58134)&  
  (tsic_r ~= 58191) & (tsic_r ~= 58192) & (tsic_r ~= 58193) &  
  (tsic_r ~= 58201) & (tsic_r ~= 58202) & (tsic_r ~= 58203) & /*Category 59*/  
  (tsic_r ~= 59111) & (tsic_r ~= 59112) &  
  (tsic_r ~= 59121) & (tsic_r ~= 59122) & (tsic_r ~= 59129) &  
  (tsic_r ~= 59131) & (tsic_r ~= 59132) &  
  (tsic_r ~= 59140) &  
  (tsic_r ~= 59201) & (tsic_r ~= 59202) & (tsic_r ~= 59203) & /*Category 60*/  
  (tsic_r ~= 60101) & (tsic_r ~= 60102) &  
  (tsic_r ~= 60201) & (tsic_r ~= 60202) & (tsic_r ~= 60203) & /*Category 61*/  
  (tsic_r ~= 61101) & (tsic_r ~= 61102) & (tsic_r ~= 61109) &  
  (tsic_r ~= 61201) & (tsic_r ~= 61202) & (tsic_r ~= 61209) &  
  (tsic_r ~= 61301) & (tsic_r ~= 61302) &  
  (tsic_r ~= 61900) & /*Category 62*/  
  (tsic_r ~= 62011) & (tsic_r ~= 62012) &
```

```

(tsic_r ~= 62021) & (tsic_r ~= 62022) & (tsic_r ~= 62023) &
(tsic_r ~= 62090) & /*Category 63*/
(tsic_r ~= 63111) & (tsic_r ~= 63112) &
(tsic_r ~= 63120) &
(tsic_r ~= 63911) & (tsic_r ~= 63912) &
(tsic_r ~= 63990)tsic_rf=1.
EXECUTE .

/*TYPE = 06*/
/*TYPE = 06, tsic_r = 68101 - 68202*/
IF(tsic_r >= 68101) & (tsic_r <= 68202) & /*Category 68*/
(tsic_r ~= 68101) & (tsic_r ~= 68102) & (tsic_r ~= 68103) & (tsic_r ~= 68104) &
(tsic_r ~= 68201) & (tsic_r ~= 68202)tsic_rf=1.
EXECUTE .

/*TYPE = 07*/
/*TYPE = 07, tsic_r = 69100 - 74909*/
IF(tsic_r >= 69100) & (tsic_r <= 74909) & /*Category 69*/
(tsic_r ~= 69100) & (tsic_r ~= 69200) & /*Category 70*/
(tsic_r ~= 70100) &
(tsic_r ~= 70201) & (tsic_r ~= 70202) & (tsic_r ~= 70209) & /*Category 71*/
(tsic_r ~= 71101) & (tsic_r ~= 71102) & (tsic_r ~= 71103) &
(tsic_r ~= 71201) & (tsic_r ~= 71202) & (tsic_r ~= 71209) & /*Category 72*/
(tsic_r ~= 72101) & (tsic_r ~= 72102) & (tsic_r ~= 72109) &
(tsic_r ~= 72200) & /*Category 73*/
(tsic_r ~= 73101) & (tsic_r ~= 73102) &
(tsic_r ~= 73200) & /*Category 74*/
(tsic_r ~= 74101) & (tsic_r ~= 74109) &
(tsic_r ~= 74200) &
(tsic_r ~= 74901) & (tsic_r ~= 74902) & (tsic_r ~= 74909)tsic_rf=1.
EXECUTE .

/*TYPE = 08*/

```

```
/*TYPE = 08, tsic_r = 77101 - 82990*/
IF(tsic_r >= 77101) & (tsic_r <= 82990) & /*Category 77*/
  (tsic_r ~= 77101) & (tsic_r ~= 77109) &
  (tsic_r ~= 77210) &
  (tsic_r ~= 77220) &
  (tsic_r ~= 77291) & (tsic_r ~= 77299) &
  (tsic_r ~= 77301) & (tsic_r ~= 77302) & (tsic_r ~= 77303) & (tsic_r ~= 77304) & (tsic_r ~= 77305) &
  (tsic_r ~= 77306) & (tsic_r ~= 77309) &
  (tsic_r ~= 77400) & /*Category 78*/
  (tsic_r ~= 78101) & (tsic_r ~= 78109) &
  (tsic_r ~= 78200) &
  (tsic_r ~= 78300) & /*Category 79*/
  (tsic_r ~= 79110) &
  (tsic_r ~= 79120) &
  (tsic_r ~= 79901) &
  (tsic_r ~= 79909) & /*Category 80*/
  (tsic_r ~= 80100) &
  (tsic_r ~= 80200) &
  (tsic_r ~= 80300) & /*Category 81*/
  (tsic_r ~= 81100) &
  (tsic_r ~= 81210) &
  (tsic_r ~= 81291) & (tsic_r ~= 81292) & (tsic_r ~= 81293) & (tsic_r ~= 81299) &
  (tsic_r ~= 81300) & /*Category 82*/
  (tsic_r ~= 82110) &
  (tsic_r ~= 82191) & (tsic_r ~= 82199) &
  (tsic_r ~= 82200) &
  (tsic_r ~= 82301) & (tsic_r ~= 82302) &
  (tsic_r ~= 82911) & (tsic_r ~= 82912) &
  (tsic_r ~= 82920) &
  (tsic_r ~= 82990)tsic_rf=1.
EXECUTE .
```

```
/*TYPE = 09*/
/*TYPE = 09, tsic_r = 90001 - 93299*/
IF(tsic_r >= 90001) & (tsic_r <= 93299) & /*Category 90*/
```

```
(tsic_r ~= 90001) & (tsic_r ~= 90002) & /*Category 91*/  
(tsic_r ~= 91011) & (tsic_r ~= 91012) &  
(tsic_r ~= 91021) & (tsic_r ~= 91022) &  
(tsic_r ~= 91031) & (tsic_r ~= 91032) &  
(tsic_r ~= 92001) & (tsic_r ~= 92009) & /*Category 93*/  
(tsic_r ~= 93111) & (tsic_r ~= 93112) &  
(tsic_r ~= 93120) &  
(tsic_r ~= 93191) & (tsic_r ~= 93192) & (tsic_r ~= 93199) &  
(tsic_r ~= 93210) &  
(tsic_r ~= 93291) & (tsic_r ~= 93292) & (tsic_r ~= 93293) & (tsic_r ~= 93299)tsic_rf=1.  
EXECUTE .
```

```
/*TYPE = 10*/  
/*TYPE = 10, tsic_r = 95110 - 96309*/  
IF(tsic_r >= 95110) & (tsic_r <= 96309) & /*Category 95*/  
(tsic_r ~= 95110) &  
(tsic_r ~= 95120) &  
(tsic_r ~= 95210) &  
(tsic_r ~= 95220) &  
(tsic_r ~= 95230) &  
(tsic_r ~= 95240) &  
(tsic_r ~= 95291) & (tsic_r ~= 95292) & (tsic_r ~= 95293) & (tsic_r ~= 95294) & (tsic_r ~= 95295) &  
(tsic_r ~= 95299) & /*Category 96*/  
(tsic_r ~= 96101) & (tsic_r ~= 96102) & (tsic_r ~= 96103) & (tsic_r ~= 96104) & (tsic_r ~= 96109) &  
(tsic_r ~= 96201) & (tsic_r ~= 96202) & (tsic_r ~= 96203) &  
(tsic_r ~= 96301) & (tsic_r ~= 96302) & (tsic_r ~= 96303) & (tsic_r ~= 96304) & (tsic_r ~= 96305) &  
(tsic_r ~= 96309)tsic_rf=1.  
EXECUTE .
```

```
/*TYPE = 11*/  
/*TYPE = 11, tsic_r = 10111 - 33200*/  
IF(tsic_r >= 10111) & (tsic_r <= 33200) & /*Category 10*/  
(tsic_r ~= 10111) & (tsic_r ~= 10112) &  
(tsic_r ~= 10120) &
```

(tsic_r ~= 10131) & (tsic_r ~= 10132) & (tsic_r ~= 10133) & (tsic_r ~= 10134) & (tsic_r ~= 10139) &
(tsic_r ~= 10211) & (tsic_r ~= 10212) &
(tsic_r ~= 10221) & (tsic_r ~= 10222) &
(tsic_r ~= 10291) & (tsic_r ~= 10292) & (tsic_r ~= 10293) & (tsic_r ~= 10294) & (tsic_r ~= 10295) &
(tsic_r ~= 10299) &
(tsic_r ~= 10301) & (tsic_r ~= 10302) & (tsic_r ~= 10303) & (tsic_r ~= 10304) & (tsic_r ~= 10305) &
(tsic_r ~= 10306) & (tsic_r ~= 10307) & (tsic_r ~= 10309) &
(tsic_r ~= 10411) & (tsic_r ~= 10412) & (tsic_r ~= 10413) & (tsic_r ~= 10414) & (tsic_r ~= 10419) &
(tsic_r ~= 10420) &
(tsic_r ~= 10491) & (tsic_r ~= 10499) &
(tsic_r ~= 10501) & (tsic_r ~= 10502) & (tsic_r ~= 10503) & (tsic_r ~= 10504) & (tsic_r ~= 10505) &
(tsic_r ~= 10509) &
(tsic_r ~= 10611) & (tsic_r ~= 10612) & (tsic_r ~= 10613) & (tsic_r ~= 10614) & (tsic_r ~= 10615) &
(tsic_r ~= 10616) & (tsic_r ~= 10617) & (tsic_r ~= 10619) &
(tsic_r ~= 10621) & (tsic_r ~= 10622) & (tsic_r ~= 10623) & (tsic_r ~= 10629) &
(tsic_r ~= 10711) & (tsic_r ~= 10712) & (tsic_r ~= 10713) &
(tsic_r ~= 10721) & (tsic_r ~= 10722) & (tsic_r ~= 10723) &
(tsic_r ~= 10731) & (tsic_r ~= 10732) & (tsic_r ~= 10733) & (tsic_r ~= 10734) & (tsic_r ~= 10739) &
(tsic_r ~= 10741) & (tsic_r ~= 10742) & (tsic_r ~= 10743) & (tsic_r ~= 10749) &
(tsic_r ~= 10751) & (tsic_r ~= 10752) &
(tsic_r ~= 10761) & (tsic_r ~= 10762) & (tsic_r ~= 10769) &
(tsic_r ~= 10771) & (tsic_r ~= 10772) & (tsic_r ~= 10773) & (tsic_r ~= 10774) & (tsic_r ~= 10775) &
(tsic_r ~= 10779) &
(tsic_r ~= 10791) & (tsic_r ~= 10792) & (tsic_r ~= 10793) & (tsic_r ~= 10794) & (tsic_r ~= 10795) &
(tsic_r ~= 10796) & (tsic_r ~= 10799) &
(tsic_r ~= 10801) & (tsic_r ~= 10802) & /*Category 11*/
(tsic_r ~= 11011) & (tsic_r ~= 11012) &
(tsic_r ~= 11021) & (tsic_r ~= 11022) & (tsic_r ~= 11029) &
(tsic_r ~= 11030) &
(tsic_r ~= 11041) & (tsic_r ~= 11042) & (tsic_r ~= 11043) & (tsic_r ~= 11044) & (tsic_r ~= 11049) &
/*Category 12*/
(tsic_r ~= 12001) & (tsic_r ~= 12002) & (tsic_r ~= 12009) & /*Category 13*/
(tsic_r ~= 13111) & (tsic_r ~= 13112) & (tsic_r ~= 13113) &
(tsic_r ~= 13121) & (tsic_r ~= 13122) & (tsic_r ~= 13129) &
(tsic_r ~= 13131) & (tsic_r ~= 13132) & (tsic_r ~= 13139) &
(tsic_r ~= 13910) &

(tsic_r ~= 13921) & (tsic_r ~= 13922) & (tsic_r ~= 13929) &
(tsic_r ~= 13930) &
(tsic_r ~= 13940) &
(tsic_r ~= 13991) & (tsic_r ~= 13992) & (tsic_r ~= 13999) & /*Category 14*/
(tsic_r ~= 14111) & (tsic_r ~= 14112) & (tsic_r ~= 14113) & (tsic_r ~= 14114) & (tsic_r ~= 14115) &
(tsic_r ~= 14116) &
(tsic_r ~= 14120) &
(tsic_r ~= 14131) & (tsic_r ~= 14139) &
(tsic_r ~= 14200) &
(tsic_r ~= 14301) & (tsic_r ~= 14302) & /*Category 15*/
(tsic_r ~= 15110) &
(tsic_r ~= 15121) & (tsic_r ~= 15122) & (tsic_r ~= 15129) &
(tsic_r ~= 15201) & (tsic_r ~= 15202) & (tsic_r ~= 15203) & (tsic_r ~= 15204) & (tsic_r ~= 15209) &
/*Category 16*/
(tsic_r ~= 16101) & (tsic_r ~= 16102) &
(tsic_r ~= 16210) &
(tsic_r ~= 16220) &
(tsic_r ~= 16230) &
(tsic_r ~= 16291) & (tsic_r ~= 16292) & (tsic_r ~= 16299) & /*Category 17*/
(tsic_r ~= 17011) & (tsic_r ~= 17012) &
(tsic_r ~= 17020) &
(tsic_r ~= 17091) & (tsic_r ~= 17092) & (tsic_r ~= 17099) & /*Category 18*/
(tsic_r ~= 18111) & (tsic_r ~= 18112) & (tsic_r ~= 18119) &
(tsic_r ~= 18121) & (tsic_r ~= 18122) &
(tsic_r ~= 18200) & /*Category 19*/
(tsic_r ~= 19100) &
(tsic_r ~= 19201) & (tsic_r ~= 19202) & (tsic_r ~= 19209) & /*Category 20*/
(tsic_r ~= 20111) & (tsic_r ~= 20112) & (tsic_r ~= 20113) & (tsic_r ~= 20114) & (tsic_r ~= 20115) &
(tsic_r ~= 20121) & (tsic_r ~= 20122) &
(tsic_r ~= 20131) & (tsic_r ~= 20132) &
(tsic_r ~= 20210) &
(tsic_r ~= 20221) & (tsic_r ~= 20222) &
(tsic_r ~= 20231) & (tsic_r ~= 20232) &
(tsic_r ~= 20291) & (tsic_r ~= 20292) & (tsic_r ~= 20293) & (tsic_r ~= 20294) & (tsic_r ~= 20299) &
(tsic_r ~= 20300) & /*Category 21*/
(tsic_r ~= 21001) & (tsic_r ~= 21002) & /*Category 22*/

(tsic_r ~= 22111) & (tsic_r ~= 22112) &
(tsic_r ~= 22191) & (tsic_r ~= 22192) & (tsic_r ~= 22193) & (tsic_r ~= 22199) &
(tsic_r ~= 22210) & (tsic_r ~= 22220) & (tsic_r ~= 22230) &
(tsic_r ~= 22291) & (tsic_r ~= 22292) & (tsic_r ~= 22299) & /*Category 23*/
(tsic_r ~= 23101) & (tsic_r ~= 23102) & (tsic_r ~= 23103) & (tsic_r ~= 23109) &
(tsic_r ~= 23910) &
(tsic_r ~= 23921) & (tsic_r ~= 23922) & (tsic_r ~= 23923) & (tsic_r ~= 23929) &
(tsic_r ~= 23931) & (tsic_r ~= 23932) & (tsic_r ~= 23933) & (tsic_r ~= 23939) &
(tsic_r ~= 23941) & (tsic_r ~= 23942) &
(tsic_r ~= 23951) & (tsic_r ~= 23952) & (tsic_r ~= 23953) & (tsic_r ~= 23954) & (tsic_r ~= 23959) &
(tsic_r ~= 23961) & (tsic_r ~= 23969) &
(tsic_r ~= 23991) & (tsic_r ~= 23992) & (tsic_r ~= 23999) & /*Category 24*/
(tsic_r ~= 24101) & (tsic_r ~= 24102) & (tsic_r ~= 24103) & (tsic_r ~= 24109) &
(tsic_r ~= 24201) & (tsic_r ~= 24202) & (tsic_r ~= 24203) & (tsic_r ~= 24204) & (tsic_r ~= 24209) &
(tsic_r ~= 24311) & (tsic_r ~= 24312) &
(tsic_r ~= 24320) & /*Category 25*/
(tsic_r ~= 25111) & (tsic_r ~= 25112) & (tsic_r ~= 25113) & (tsic_r ~= 25119) &
(tsic_r ~= 25121) & (tsic_r ~= 25122) & (tsic_r ~= 25129) &
(tsic_r ~= 25130) &
(tsic_r ~= 25201) & (tsic_r ~= 25209) &
(tsic_r ~= 25910) &
(tsic_r ~= 25921) & (tsic_r ~= 25922) &
(tsic_r ~= 25931) & (tsic_r ~= 25932) & (tsic_r ~= 25939) &
(tsic_r ~= 25941) & (tsic_r ~= 25949) &
(tsic_r ~= 25951) & (tsic_r ~= 25952) &
(tsic_r ~= 25991) & (tsic_r ~= 25992) & (tsic_r ~= 25993) & (tsic_r ~= 25999) & /*Category 26*/
(tsic_r ~= 26101) & (tsic_r ~= 26102) & (tsic_r ~= 26103) & (tsic_r ~= 26104) & (tsic_r ~= 26109) &
(tsic_r ~= 26201) & (tsic_r ~= 26202) & (tsic_r ~= 26203) & (tsic_r ~= 26209) &
(tsic_r ~= 26301) & (tsic_r ~= 26302) & (tsic_r ~= 26303) & (tsic_r ~= 26309) &
(tsic_r ~= 26401) & (tsic_r ~= 26402) & (tsic_r ~= 26403) & (tsic_r ~= 26409) &
(tsic_r ~= 26511) & (tsic_r ~= 26512) &
(tsic_r ~= 26521) & (tsic_r ~= 26529) &
(tsic_r ~= 26600) &
(tsic_r ~= 26701) & (tsic_r ~= 26702) & (tsic_r ~= 26703) &
(tsic_r ~= 26800) & /*Category 27*/
(tsic_r ~= 27101) & (tsic_r ~= 27102) & (tsic_r ~= 27103) &

(tsic_r ~= 27200) &
(tsic_r ~= 27310) & (tsic_r ~= 27320) & (tsic_r ~= 27330) &
(tsic_r ~= 27401) & (tsic_r ~= 27409) &
(tsic_r ~= 27501) & (tsic_r ~= 27502) & (tsic_r ~= 27503) &
(tsic_r ~= 27901) & (tsic_r ~= 27902) & (tsic_r ~= 27909) & /*Category 28*/
(tsic_r ~= 28110) &
(tsic_r ~= 28120) &
(tsic_r ~= 28131) & (tsic_r ~= 28132) &
(tsic_r ~= 28140) &
(tsic_r ~= 28150) &
(tsic_r ~= 28160) &
(tsic_r ~= 28170) &
(tsic_r ~= 28180) &
(tsic_r ~= 28191) & (tsic_r ~= 28199) &
(tsic_r ~= 28211) & (tsic_r ~= 28219) &
(tsic_r ~= 28221) & (tsic_r ~= 28229) &
(tsic_r ~= 28230) &
(tsic_r ~= 28240) &
(tsic_r ~= 28250) &
(tsic_r ~= 28261) & (tsic_r ~= 28269) &
(tsic_r ~= 28291) &
(tsic_r ~= 28292) & (tsic_r ~= 28299) & /*Category 29*/
(tsic_r ~= 29101) & (tsic_r ~= 29102) & (tsic_r ~= 29103) & (tsic_r ~= 29104) & (tsic_r ~= 29109) &
(tsic_r ~= 29201) & (tsic_r ~= 29202) & (tsic_r ~= 29203) &
(tsic_r ~= 29301) & (tsic_r ~= 29302) & (tsic_r ~= 29309) & /*Category 30*/
(tsic_r ~= 30110) &
(tsic_r ~= 30120) &
(tsic_r ~= 30200) &
(tsic_r ~= 30300) &
(tsic_r ~= 30400) &
(tsic_r ~= 30911) & (tsic_r ~= 30912) &
(tsic_r ~= 30921) & (tsic_r ~= 30922) &
(tsic_r ~= 30990) & /*Category 31*/
(tsic_r ~= 31001) & (tsic_r ~= 31002) & (tsic_r ~= 31003) & (tsic_r ~= 31009) & /*Category 32*/
(tsic_r ~= 32111) & (tsic_r ~= 32112) & (tsic_r ~= 32113) &
(tsic_r ~= 32120) &


```
(tsic_r ~= 32201) & (tsic_r ~= 32209) &
(tsic_r ~= 32301) & (tsic_r ~= 32302) & (tsic_r ~= 32303) & (tsic_r ~= 32309) &
(tsic_r ~= 32401) & (tsic_r ~= 32402) & (tsic_r ~= 32409) &
(tsic_r ~= 32501) & (tsic_r ~= 32502) &
(tsic_r ~= 32901) & (tsic_r ~= 32902) & (tsic_r ~= 32903) & (tsic_r ~= 32904) & (tsic_r ~= 32909) &
/*Category 33*/
(tsic_r ~= 33110) &
(tsic_r ~= 33121) & (tsic_r ~= 33122) &
(tsic_r ~= 33131) & (tsic_r ~= 33132) & (tsic_r ~= 33133) &
(tsic_r ~= 33141) & (tsic_r ~= 33142) & (tsic_r ~= 33149) &
(tsic_r ~= 33151) & (tsic_r ~= 33152) & (tsic_r ~= 33159) &
(tsic_r ~= 33190) &
(tsic_r ~= 33200)tsic_rf=1.
EXECUTE .
```

```
/*TYPE = 12*/
/*TYPE = 12, tsic_r = 37000 - 39000*/
IF(tsic_r >= 37000) & (tsic_r <= 39000) & /*Category 37*/
(tsic_r ~= 37000) & /*Category 38*/
(tsic_r ~= 38110) & (tsic_r ~= 38120) &
(tsic_r ~= 38211) & (tsic_r ~= 38212) & (tsic_r ~= 38213) & (tsic_r ~= 38219) &
(tsic_r ~= 38221) & (tsic_r ~= 38222) & (tsic_r ~= 38300) &
(tsic_r ~= 39000)tsic_rf=1.
EXECUTE .
```

```
/*TYPE = 13*/
/*TYPE = 13, tsic_r = 41001 - 43909*/
IF(tsic_r >= 41001) & (tsic_r <= 43909) & /*Category 41*/
(tsic_r ~= 41001) & (tsic_r ~= 41002) & /*Category 42*/
(tsic_r ~= 42101) & (tsic_r ~= 42102) &
(tsic_r ~= 42201) & (tsic_r ~= 42202) &
(tsic_r ~= 42901) & (tsic_r ~= 42909) & /*Category 43*/
(tsic_r ~= 43110) &
(tsic_r ~= 43120) &
```

```
(tsic_r ~= 43210) &
(tsic_r ~= 43221) & (tsic_r ~= 43222) & (tsic_r ~= 43223) &
(tsic_r ~= 43291) & (tsic_r ~= 43299) &
(tsic_r ~= 43301) & (tsic_r ~= 43302) & (tsic_r ~= 43303) & (tsic_r ~= 43309) &
(tsic_r ~= 43901) & (tsic_r ~= 43909)tsic_rf=1.
EXECUTE .
```

```
/*TYPE = 14*/
/*TYPE = 14, tsic_r = 49110 - 52299*/
IF(tsic_r >= 49110) & (tsic_r <= 52299) & /*Category 49*/
  (tsic_r ~= 49201) & (tsic_r ~= 49202) & (tsic_r ~= 49203) & (tsic_r ~= 49204) & (tsic_r ~= 49209) &
  (tsic_r ~= 49310) &
  (tsic_r ~= 49329) &
  (tsic_r ~= 49331) & (tsic_r ~= 49332) & (tsic_r ~= 49333) & (tsic_r ~= 49334) & (tsic_r ~= 49339) &
/*Category 52*/
  (tsic_r ~= 52101) & (tsic_r ~= 52102) & (tsic_r ~= 52109) &
  (tsic_r ~= 52291) tsic_rf=1.
EXECUTE .
```

```
/*TYPE = 15*/
/*TYPE = 15, tsic_r = 86101 - 86102*/
IF(tsic_r >= 86101) & (tsic_r <= 86102) & /*Category 86*/
  (tsic_r ~= 86101) & (tsic_r ~= 86102)tsic_rf=1.
EXECUTE .
```

```
IF((tsic_r >= 10803) & (tsic_r <= 11010))|((tsic_r >= 11050) & (tsic_r <= 12000))|((tsic_r >= 12010) &
(tsic_r <= 13110))|
  ((tsic_r >= 14000) & (tsic_r <= 14101))|((tsic_r >= 14303) & (tsic_r <= 15109))|((tsic_r >= 15210) &
(tsic_r <= 16100))|
  ((tsic_r >= 16300) & (tsic_r <= 17010))|((tsic_r >= 17100) & (tsic_r <= 18110))|((tsic_r >= 18201) &
(tsic_r <= 19099))|
  ((tsic_r >= 19210) & (tsic_r <= 20110))|((tsic_r >= 20301) & (tsic_r <= 21000))|((tsic_r >= 21003) &
(tsic_r <= 22110))|
```

((tsic_r >= 22300) & (tsic_r <= 23100))|((tsic_r >= 24000) & (tsic_r <= 24100))|((tsic_r >= 24321) & (tsic_r <= 25110))|
((tsic_r >= 26000) & (tsic_r <= 26100))|((tsic_r >= 26801) & (tsic_r <= 27100))|((tsic_r >= 27910) & (tsic_r <= 28109))|
((tsic_r >= 28300) & (tsic_r <= 29100))|((tsic_r >= 29310) & (tsic_r <= 30109))|((tsic_r >= 30991) & (tsic_r <= 31000))|
((tsic_r >= 31010) & (tsic_r <= 32110))|((tsic_r >= 32910) & (tsic_r <= 33109))|((tsic_r >= 33201) & (tsic_r <= 36999))|
((tsic_r >= 37001) & (tsic_r <= 38109))|((tsic_r >= 38301) & (tsic_r <= 38999))|((tsic_r >= 39001) & (tsic_r <= 41000))|
((tsic_r >= 41003) & (tsic_r <= 42100))|((tsic_r >= 42910) & (tsic_r <= 43109))|((tsic_r >= 43910) & (tsic_r <= 45100))|
((tsic_r >= 45406) & (tsic_r <= 46100))|((tsic_r >= 46901) & (tsic_r <= 47110))|((tsic_r >= 48000) & (tsic_r <= 49109))|
((tsic_r >= 49399) & (tsic_r <= 52100))|((tsic_r >= 52300) & (tsic_r <= 55100))|((tsic_r >= 55910) & (tsic_r <= 56100))|
((tsic_r >= 56305) & (tsic_r <= 58110))|((tsic_r >= 58204) & (tsic_r <= 59110))|((tsic_r >= 59204) & (tsic_r <= 60100))|
((tsic_r >= 60204) & (tsic_r <= 61100))|((tsic_r >= 61901) & (tsic_r <= 62010))|((tsic_r >= 62091) & (tsic_r <= 63110))|
((tsic_r >= 63991) & (tsic_r <= 68100))|((tsic_r >= 68203) & (tsic_r <= 69099))|((tsic_r >= 69201) & (tsic_r <= 70099))|
((tsic_r >= 70210) & (tsic_r <= 71100))|((tsic_r >= 68203) & (tsic_r <= 69099))|((tsic_r >= 69201) & (tsic_r <= 70099))|
((tsic_r >= 71210) & (tsic_r <= 72100))|((tsic_r >= 72201) & (tsic_r <= 73100))|((tsic_r >= 73201) & (tsic_r <= 74100))|
((tsic_r >= 74910) & (tsic_r <= 77100))|((tsic_r >= 77401) & (tsic_r <= 78100))|((tsic_r >= 78301) & (tsic_r <= 79109))|
((tsic_r >= 79910) & (tsic_r <= 80099))|((tsic_r >= 80301) & (tsic_r <= 81099))|((tsic_r >= 81301) & (tsic_r <= 82109))|
((tsic_r >= 82991) & (tsic_r <= 86100))|((tsic_r >= 86103) & (tsic_r <= 90000))|((tsic_r >= 90003) & (tsic_r <= 91010))|
((tsic_r >= 91033) & (tsic_r <= 92000))|((tsic_r >= 92010) & (tsic_r <= 93110))|((tsic_r >= 93300) & (tsic_r <= 95109))|
((tsic_r >= 95300) & (tsic_r <= 96100))|((tsic_r >= 96310) & (tsic_r <= 99999))|((tsic_r >= 00000) & (tsic_r <= 10110))tsic_rf=1.

EXECUTE .

SORT CASES BY

tsic_rf (D) .